PTO/SB/81 (04-05)
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Application Number	10/721,843					
Filing Date	November 25, 2003					
First Named Inventor	Wurzer, Steve G.					
Title	Programmable Phase-Locked Loop					
Art Unit	2816					
Examiner Name	Nguyen, Hai L.					
Attorney Docket Number	03-2104					

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Assignee of record of the entire interest. See 37 CFR 3.71. Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96)											
SIGNATURE of Applicant or Assignee of Record											
Signature		The	RGU						Date	30 JUN 05	
Name	Timothy	R. Croll							Telephone	(408) 433-7625	
Title and Company	Senior	Corporate	Counsel, IP								
NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below*.											
*Total of	1	forms are	submitted.								

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PTO/SB/96 (09-04)

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STATEMENT UNDER 37 CFR 3.73(b)						
Applicant/Patent Owner: LSI Logic Corporation						
Application No./Patent No.: 10/721,843	Filed/Issue Date: November 25, 2003					
Entitled: Programmable Phase-Locked Loop						
LSI Logic Corporation , a (Name of Assignee)	Corporation (Type of Assignee, e.g., corporation, partnership, un	iversity, government agency, etc.)				
states that it is: 1. the assignee of the entire right, title, and interes	t; or					
2. an assignee of less than the entire right, title and interest. The extent (by percentage) of its ownership interest is %						
in the patent application/patent identified above by virtue of either:						
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OR B. A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as shown below:						
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The undersigned (whose title is supplied below) is auth	orized to act on behalf of the assignee.	30JUN 05				
Signature	-	Date				
Timothy R. Croll		(408) 433-7625				
Printed or Typed Name		Telephone Number				
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